

Form PTO-1449 (REV. 8-83)		US Dept. of Commerc PATENT & TRADEMARK OFFICE		109503 U.S. N				JCATION NO. National Stage of JP00/07175			
INFORMATION DISCLOSURE STATEMENT											
(Use several sheets if necessary)				APPLICANT Yojiro MATSUEDA							
					FILING DATE June 18, 2001				-		
U.S. PATENT DOCUMENTS											
EXAMINER INITIAL		DOCUMENT NUMBER	1	DATE		NAME		CLASS	SUB		
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		FOREIG	N PA	TENT DO	CUMENTS			1			
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JN		JP 2000-227608 A (w/abstract)	08-1	15-2000	Japan						
JN		JP 8-227283 A (w/abstract)	09-0	3-1996	Japan			-			
JN		JP 10-222136 A (w/abstract) .	08-21-1998		Japan		,				
TN		JP 11-65536 A (w/abstract)	03-0	9-1999	Japan			-			
JN		JP 63-229430 A (w/abstract)	09-2	26-1988	Japan						
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EXAMINER DATE CONSIDERED 10-14-03											
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.											

Sheet 1 of 1

Form PTO-1449 (REV. 8-83) PATENT & TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT				ATTY DO 109503	OCKET NO.		APPLICATION NO. 09/868,322				
(Use several sheets if necessary)					APPLICANT Yojiro MATSUEDA						
					FILING DATE June 18, 2001			GROUP 2674			
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5	1	6,873,312 B2	03-29-2005		Matsueda						
JN.	2	6,025,822	02-15-2000		Motegi et al.						
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ZV	4	EP 0456394 A2	11-13-1991		Europe		1				
IN	5	EP 0488851 A1 w/ English Abstract	06-03-1992		Europe		-				
20	6	EP 0286309 A2	10-12-1988		Europe						
ZM	7	JP-A-11-058065 w/ English Abstract	03-02-1999		Japan						
Tr.	8	JP-A-08-227283 w/ English Abstract	09-03-1996		Japan						
ON	9	JP-A-281636 w/ English Abstract	10-2	27-1995	Japan						
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IN	10	R. Abbott et al., "Equipping a Line of Memories With Spare Cells," Electronics, VNU Business Publications, vol. 54, no. 15, pgs. 127-130, July 1981									
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Date: July 13, 2005